<ABSTRACT>

A multi-stage pipelined AD converter has n stages of conversion units, such as a first conversion unit, a second conversion unit, an (n-1)th conversion unit, and an nth conversion unit, which successively convert an analog signal into a digital signal each by several bits starting from the most significant bit. Each of the converted digital signals of several bits is combined in a digital output circuit. A first voltage source supplies a higher voltage than a second voltage source. The first voltage source supplies a high voltage to the first stage or the first conversion unit, while the second voltage source supplies a low voltage to the second and subsequent stages of the second conversion unit to the nth conversion unit which require a lower analog accuracy.